

F I G. 1

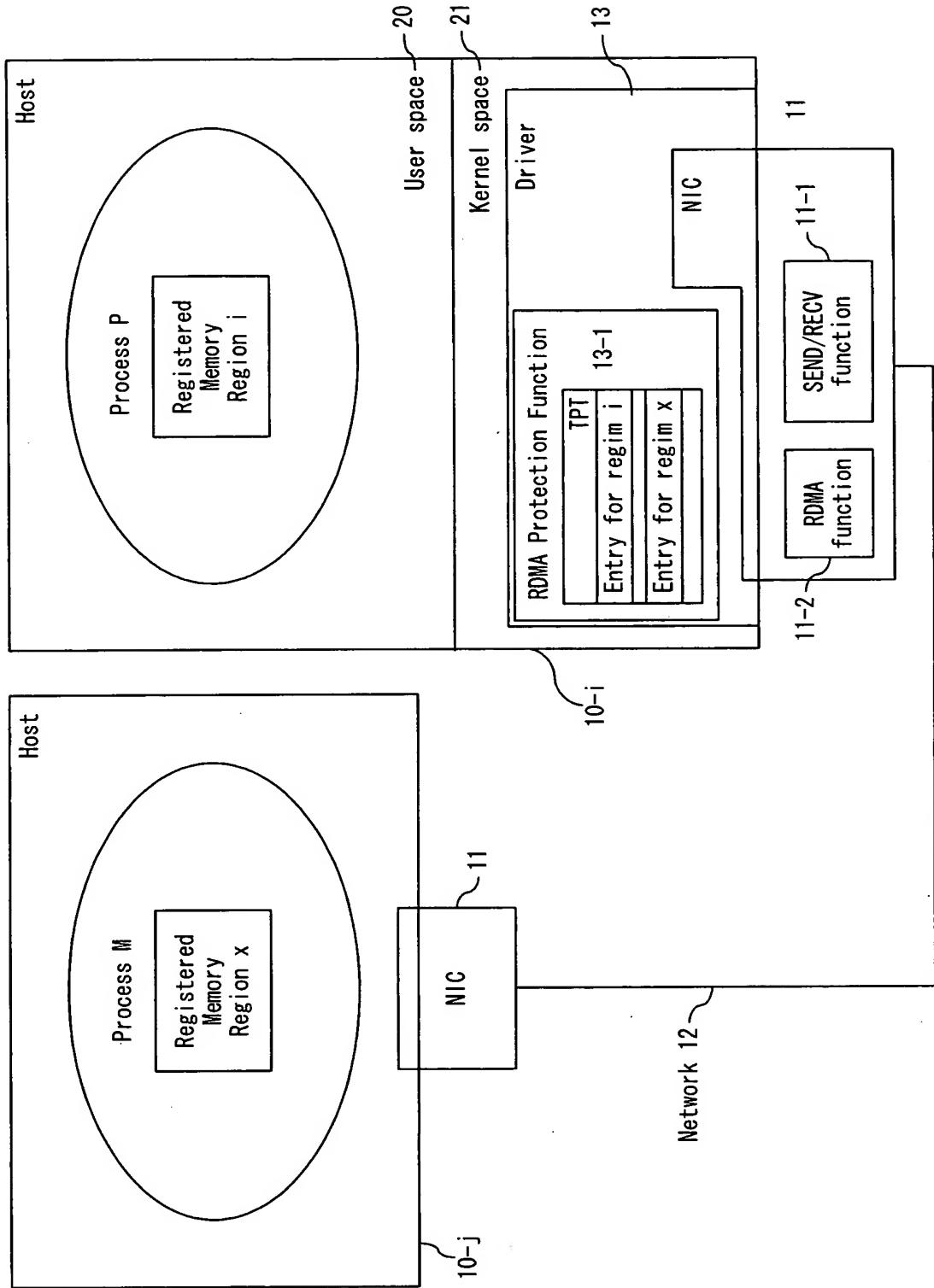


FIG. 2

TPT on Node 0

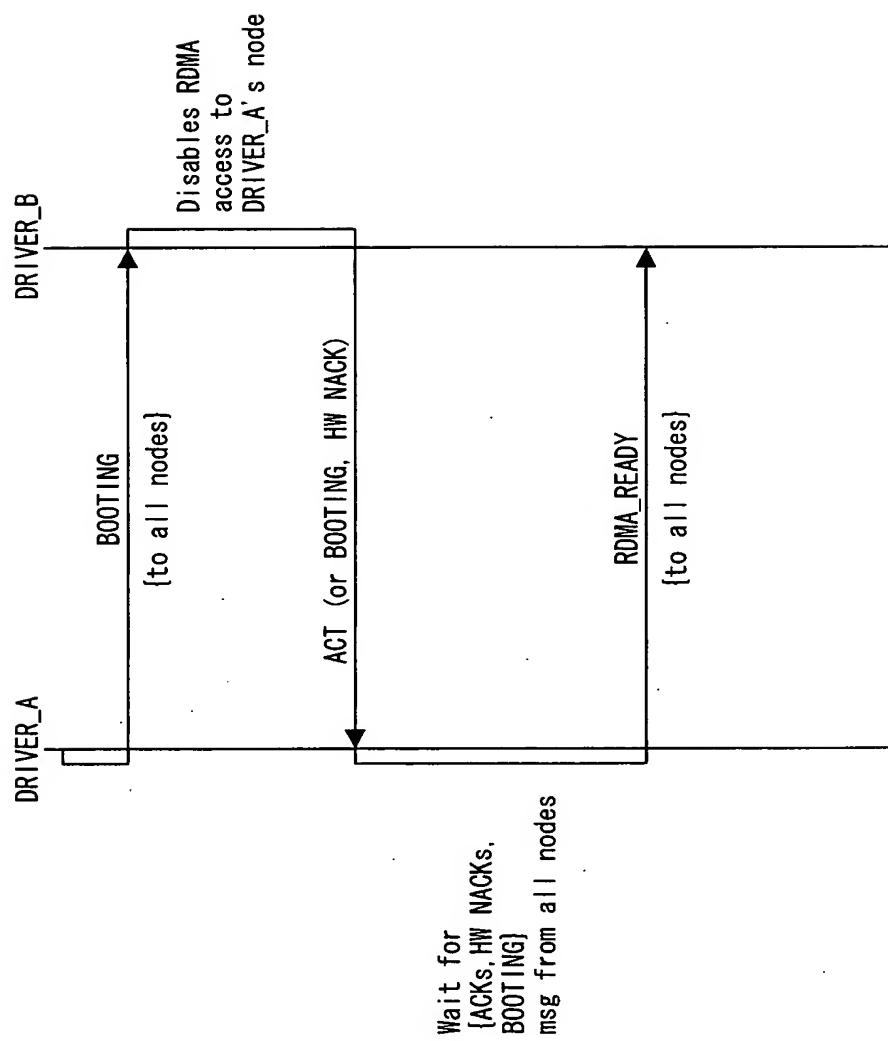
(a)

(b)

Entry Format

Entry #	Logical Address	Physical Address	Length	Protection	Attributes
---------	-----------------	------------------	--------	------------	------------

3
G.
I.
E.



F I G. 4

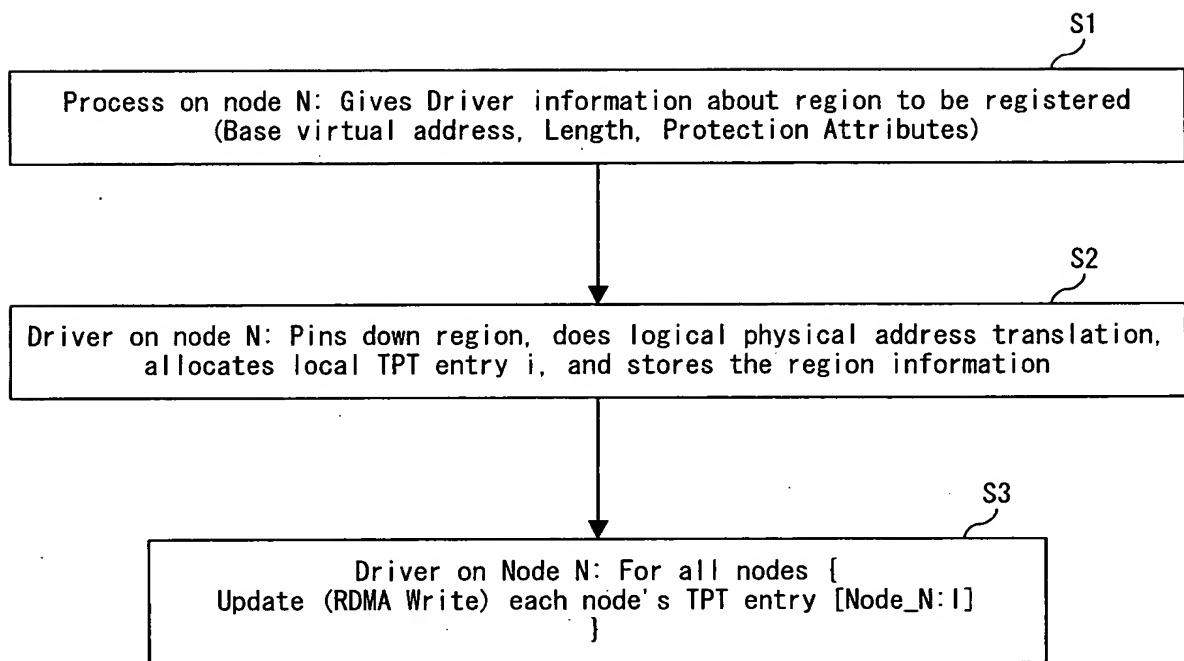
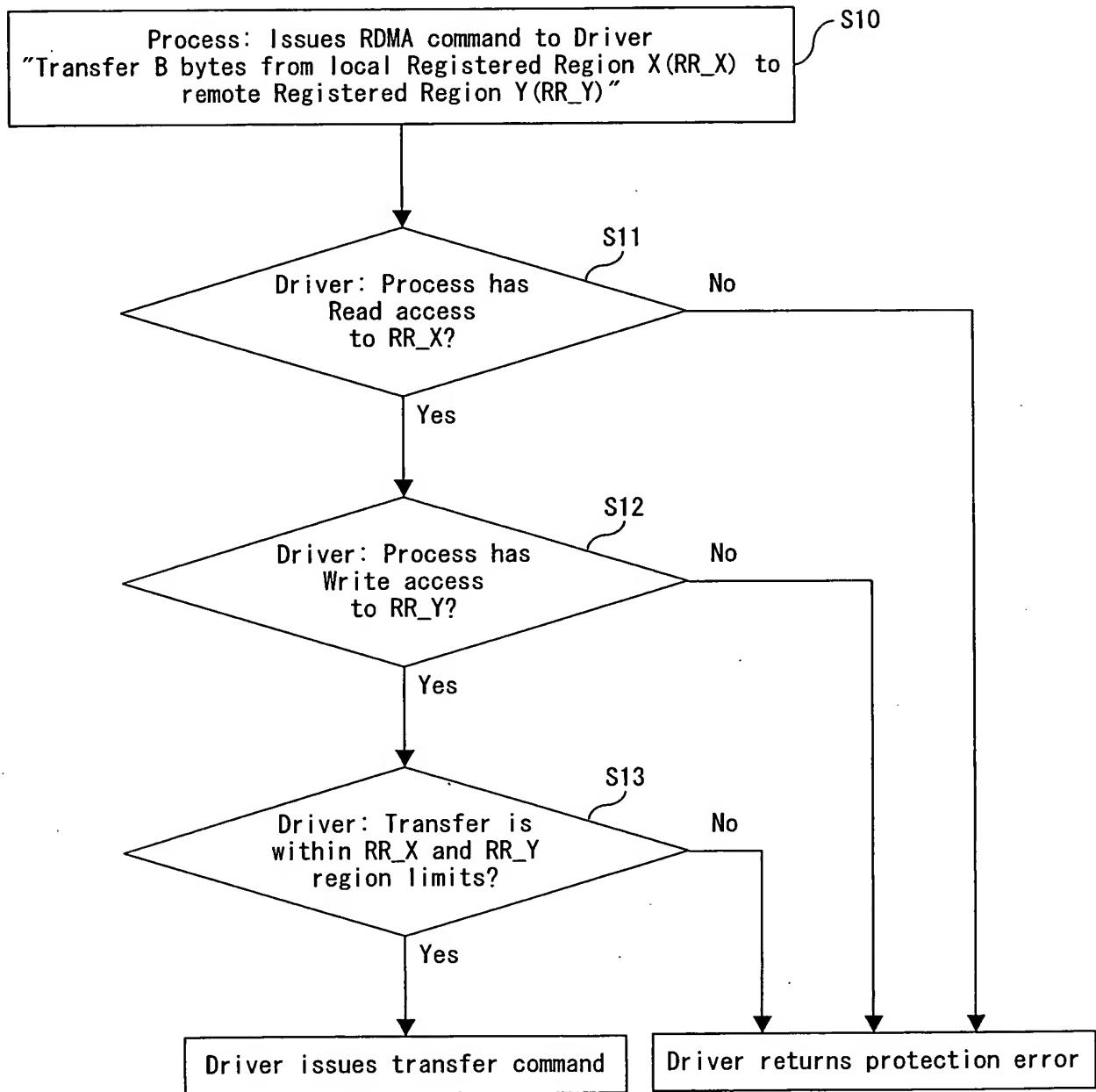


FIG. 5



F I G. 6

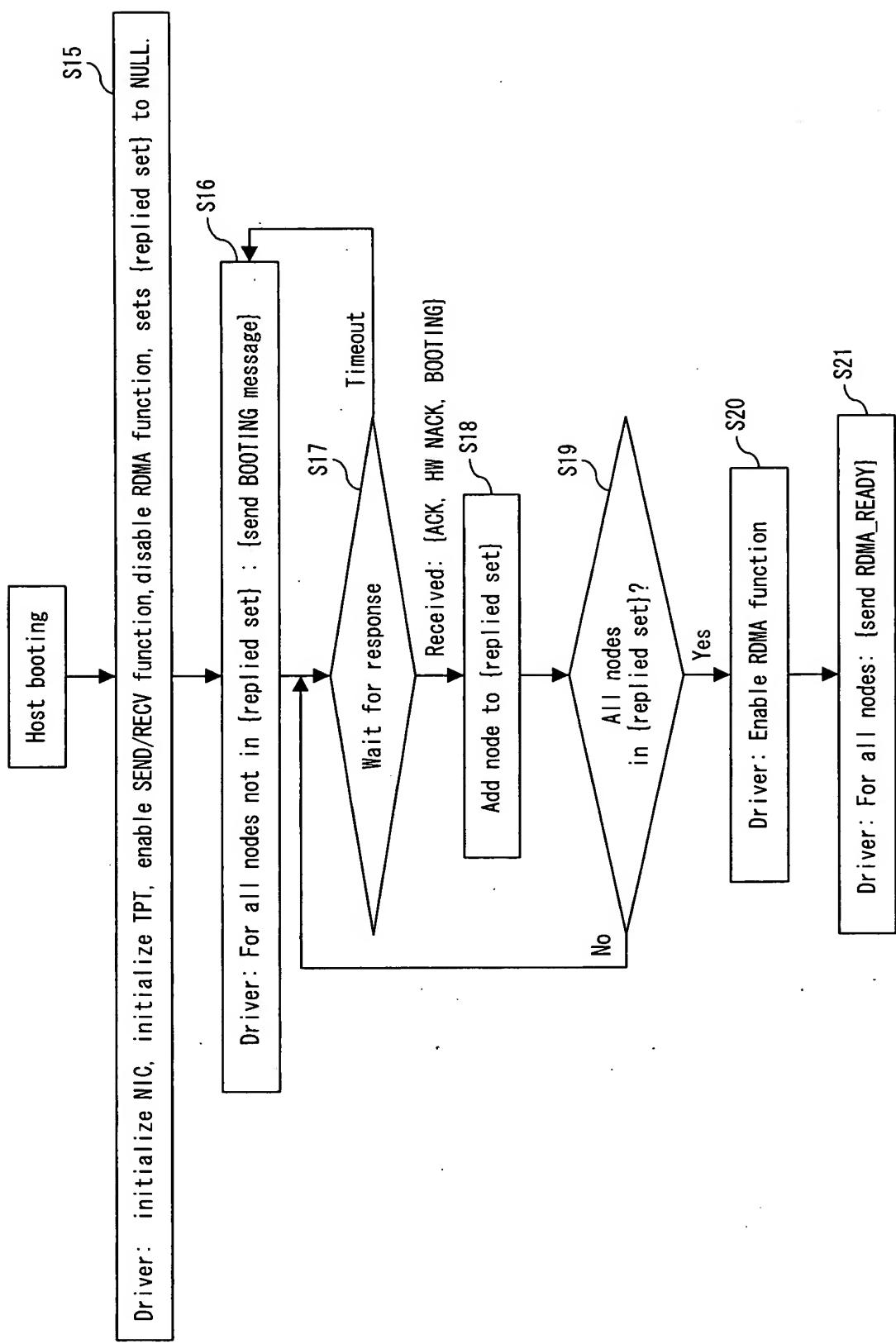


FIG. 7

